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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

3

<p style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>				Complete if Known	
Sheet	1	of	2	Application Number	09/751747
				Filing Date	12/29/00
				First Named Inventor	LAWRENCE H. HUDEPOHL
				Group Art Unit	2183 2141
				Examiner Name	SHANE P. GERSTL W/T REAST
				Attorney Docket Number	MIPS.0105-00-US

U.S. PATENT DOCUMENTS

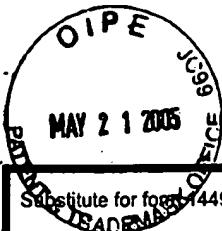
FOREIGN PATENT DOCUMENTS

Examiner Signature	W. TREAT	Date Considered	12/16/05
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PTO/SB/08A (08-03)

Approved for use through 10/31/2002. OMB 0651-0031

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~~Substitute for form 1449A/PTO~~

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 2 of 2

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
Sheet	2	of	2	Application Number	09/751747
				Filing Date	12/29/00
				First Named Inventor	LAWRENCE H. HUDEPOHL
				Group Art Unit	2483 2 (S)
				Examiner Name	SHANE F. GERSTL W. TREAT
				Attorney Docket Number	MIPS.0105-00-US

NON PATENT LITERATURE DOCUMENTS

Examiner Signature	W. TREAT	Date Considered	12/8/05
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PTO/SB/08A (08-03)

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	
(use as many sheets as necessary)				Filing Date	12/29/00
				First Named Inventor	Lawrence H. Hudepohl
				Group Art Unit	2183-2141
				Examiner Name	Shane F. Gerstl IN-TREAT
Sheet	1	of	1	Attorney Docket Number	MIPS.0105-00-US

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JWKT		MIPS Technologies, Inc. <i>MIPS64 5Kc Processor Core Datasheet</i> . Rev. 1.7.4. December 14, 1999. Mountain View, CA.	

**Examiner
Signature**

W. TREAT

Date Considered

62/48/05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/751747
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				First Named Inventor	Lawrence H. Hudepohl
				Group Art Unit	2163 248L
				Examiner Name	Shane F. Gerstl <i>W. T. REAT</i>
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JWMT		MIPS TECHNOLOGIES, INC. "MIPS64 5Kc™ Processor Core User's Manual. Rev. 1.0.1. 12/14/99. Page 247-276. MIPS Technologies, Inc. Mountain View, CA, USA	

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				Group Art Unit	2482 2141
				Examiner Name	SHANE F. GERSTL JR. TR EAST
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WMT	AA	BRUB, ROLF-JURGEN. <i>RISC, The MIPS-R3000 Family</i> . Berlin and Munich: Siemens Aktiengesellschaft., 1991. pp51-65	
WMT	AB	RIORDAN, TOM. <i>MIPS R3010 Floating-point Coprocessor Interface</i> . MIPS Computer Systems, Inc. October 27, 1988.	
WMT	AC	<i>LR3010 and LR3010A, MIPS Floating-Point Accelerator User's Manual</i> . LSI Logic Corporation. 1991.	
WMT	AD	<i>LR3000 and LR3000A, MIPS RISC Microprocessor User's Manual</i> . LSI Logic Corporation. 1991. pp.i to 1-16 and 9-1 to 9-10.	

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